

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. . . . . 08/530,661  
Filing Date . . . . . September 20, 1995  
Inventor . . . . . Brent Keeth  
Assignee . . . . . Micron Technology, Inc.  
Group Art Unit . . . . . 2503  
Examiner . . . . . N. Kelly  
Attorney's Docket No. . . . . MI22-356  
Title: Semiconductor Memory Circuit

NOTICE OF APPEAL

To: Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

RECEIVED

DEC 11 1997

From: David G. Latwesen (Tel. 509-624-4276; Fax 509-838-3424)  
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GROUP 2500

Applicant hereby appeals to the Board of Patent Appeals from the  
decision of the Examiner dated September 23, 1997, in which the  
Examiner rejected the pending claims 6-10, 18-19, 22-23 and 25-26.

By filing of this Notice of Appeal, a timely response has been  
made to the above outstanding Office Action, and it is therefore  
believed that no extension fees are required.

A check in the amount of \$310.00 in payment of the fee required  
under 37 C.F.R. §1.17(e) is attached. If such amount is insufficient,  
please charge any additional amount to Deposit Account No. 23-0925.

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Respectfully submitted,

Dated: \_\_\_\_\_

By: \_\_\_\_\_

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